**INTEGRATION OF OPENCV AND CYCLONE V HYBRID ARM AND FPGA SYSTEM ON A CHIP FOR FACE DETECTION APPLICATION**

an

UNDERGRADUATE THESIS PROPOSAL

Presented to

The Faculty of Electrical

Electronics and Communications Engineering

MSU – Iligan Institute of Technology

Iligan City

In partial fulfillment

Of the requirements for the Degree

BACHELOR OF SCIENCE IN ELECTRONICS AND

COMMUNICATIONS ENGINEERING

Submitted by:

**GIL MICHAEL E. REGALADO**

Adviser

**PROF. JEFFERSON A. HORA**

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